

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	15996	(field programmable gate array) or (programmable logic device) or PLD or FPGA	USPAT	ADJ	OFF	2007/01/22 14:50
L2	1314	(configur\$5 near3 (memory or data or frame)) with l1	USPAT	ADJ	OFF	2007/01/22 14:52
L3	93558	(edc or (error detect\$3) or crc or parity or cehcksum)	USPAT	ADJ	OFF	2007/01/22 14:53
L4	25	l2 with l3	USPAT	ADJ	OFF	2007/01/22 14:57
L5	12	l2 same l3 same (register or buffer or latch)	USPAT	ADJ	OFF	2007/01/22 15:44
L6	132724	(frame or packet or data) near3 (register or buffer or latch)	USPAT	ADJ	OFF	2007/01/22 15:27
L7	130	l1 with l6 with configur\$5	USPAT	ADJ	OFF	2007/01/22 15:30
L8	130	l1 with l6 with configur\$5 with l6	USPAT	ADJ	OFF	2007/01/22 15:31
L9	0	l1 with l6 with configur\$5 with l3	USPAT	ADJ	OFF	2007/01/22 15:32
L10	16	l1 same l6 same configur\$5 same l3	USPAT	ADJ	OFF	2007/01/22 15:32
L11	0	(configur\$5 near4 l1) with l3 with l6	USPAT	ADJ	OFF	2007/01/22 15:33
L12	9	(configur\$5 near4 l1) same l3 same l6	USPAT	ADJ	OFF	2007/01/22 15:35
L13	61	(configur\$5 near4 l1) same l3	USPAT	ADJ	OFF	2007/01/22 15:42
L14	0	l13 same (error count\$3)	USPAT	ADJ	OFF	2007/01/22 15:36
L15	20	l13 same count\$3	USPAT	ADJ	OFF	2007/01/22 15:42
L16	0	l2 same l3 same (error count\$3)	USPAT	ADJ	OFF	2007/01/22 15:45
L17	14	l2 same l3 same count\$3	USPAT	ADJ	OFF	2007/01/22 15:45